

모듈작업사양서

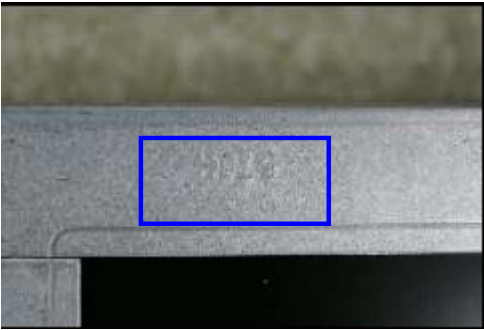
양산 / 개발 / 평가

제 품 명	LT1570HD01-0
작성일자	
개정번호	

[BLU 뒷면 외관사양]



[라벨 및 각인]

구분	내용
제품LABEL	 Product label showing various codes and barcodes. The label includes the text: T C UL US, LT1570HD01, 0743, P7BW, 8H7J0IHM0E 001, SSLZDS0, 99TJ7TJ77AP0090 ZB, and MADE IN KOREA.
B/L LABEL	 B/L label showing codes and a barcode. The label includes the text: LJ96-04060A, LT1570HD01, and H16830PSHE53H0024.
T/C 각인 [좌상측]	
	570H
T/C 각인 [우하단]	
	570H
[추가]	-

4. 검사 및 구동사양 [TEST & OPERATION]

[설비 SETTING 참조사항]

VBL	INV TYPE	형태	양측
		A-DIM 전압	Ex Pwm (80%)
	IP TYPE	형태	-
		PWM DUTY	-
	SHAKE / LAMP수		

해상도	1920*1080	Clock(Hz)		60
구동방식	DUAL	BIT		8
AGING 패턴		HVS		유
POL 사양	Anti Glare	Flicker	방식	I2C
BIST CONTROL	X		패턴	Green dot

[TIMMING]

THD	1920	TVD	1080
THP	94	TVP	15
THS	186	TVS	30
TH	2200	TV	1125

[INVERTER PIN MAP]



PIN NO.	PIN Configuration (FUNCTION)
1	Vin (24V)
2	Vin (24V)
3	Vin (24V)
4	Vin (24V)
5	Vin (24V)
6	GND
7	GND
8	GND
9	GND
10	GND
11	Reserved
12	ENA (BACKLIGHT ON ~ OFF / ON : 2.4 - 5.5V, OFF: -0.3 - 0.8V)
13	NC(No Connection)
14	Ex_PWM(External PWM Signal Input)

[SIGNAL PIN MAP]

51Pin

No	Signal		No	Signal	
1	Power	DC power supply $V_{DD}(+12[V])$	26	RE[0]P	Even LVDS Signal +
2	Power	DC power supply $V_{DD}(+12[V])$	27	RE[1]N	Even LVDS Signal -
3	Power	DC power supply $V_{DD}(+12[V])$	28	RE[1]P	Even LVDS Signal +
4	Power	DC power supply $V_{DD}(+12[V])$	29	RE[2]N	Even LVDS Signal -
5	Power	DC power supply $V_{DD}(+12[V])$	30	RE[2]P	Even LVDS Signal +
6	GND	GND	31	GND	GND
7	GND	GND	32	RECLK-	Even LVDS Clock -
8	GND	GND	33	RECLK+	Even LVDS Clock +
9	GND	GND	34	GND	GND
10	RO[0]N	Odd LVDS Signal -	35	RE[3]N	Even LVDS Signal -
11	RO[0]P	Odd LVDS Signal +	36	RE[3]P	Even LVDS Signal +
12	RO[1]N	Odd LVDS Signal -	37	NC	Not Connected
13	RO[1]P	Odd LVDS Signal +	38	NC	Not Connected
14	RO[2]N	Odd LVDS Signal -	39	GND	GND
15	RO[2]P	Odd LVDS Signal +	40	SCL	I2C SCL
16	GND	GND	41	SDA	I2C SDA
17	ROCLK-	Odd LVDS Clock -	42	NC	Not Connected
18	ROCLK+	Odd LVDS Clock +	43	B-INT	Bus release
19	GND	GND	44	NC	Not Connected
20	RO[3]N	Odd LVDS Signal -	45		
21	RO[3]P	Odd LVDS Signal +	46		
22	NC	Not Connected	47		
23	NC	Not Connected	48		
24	GND	GND	49		
25	RE[0]N	Even LVDS Signal -	50		
			51		

[전기적 특성]

(1) PANEL

항 목	MIN.	TYP.	MAX.	단위	비 고
입력 전압	10.8	12	13.2	V	White pattern
소비 전류	－	1.455	1.550	A	checker
소비 전력	－	17.46	20.46	W	checker

(2) B/L :

항 목	MIN.	TYP.	MAX.	단위	비 고
입력 전압	22	24	26	V	
소비 전류	－	15.3 (saturation)	18.1 (구동초기)	A	
소비 전력	－	367	470.6	W	

(3) TOTAL = (1) + (2)

항 목	MIN.	TYP.	MAX.	단위	비 고
입력 전압	32.8	36	39.2	V	
소비 전류	－	16.755	19.650	A	
소비 전력	－	384.46	491.06	W	

[E-SPEC (C-PBA & PANEL / BLU 구동사양)]

(1) C-PBA & PANEL 구동사양

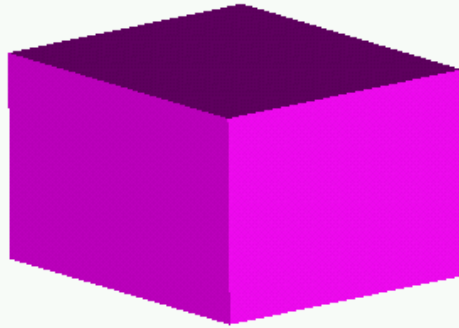
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Power Supply Voltage	VDD	10	12	14	V	(1)
Power Consumption	(a) Black	-	750	1180	mA	(2),(3)
	(b) White	-	720	1150	mA	
	(c) N-pattern	-	750	1200	mA	
	(d) Checker	-	1455	1550	mA	
Vsync Frequency	fV	50	60	62	Hz	
Hsync Frequency	fH	55	67.5	72	kHz	
Main Frequency	fDCLK	60	74.25	80	MHz	
Rush Current	IRUSH	-	-	5	A	(4)

(2) Inverter 구동사양

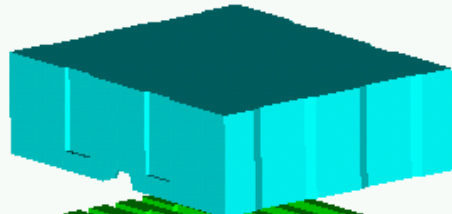
Items	Symbol	Conditions	Specifications			Unit	비고
Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.
입력 전압	Vin	-	22	24	26	V	Ta=25℃
입력 전류(Inverter전체)	Iin	Vin=24V, PDIM=100%	-	16	21	Adc	After 2hour Warm-up, Note (1)
출력 전류 (Lamp 날개)	Io,max	Vin=24V, PDIM=100%	5.5	6.0	6.5	mArms	출력 전류 (Lamp 날개)
주파수	FL	Vin=24V, PDIM=100%	45	49	52	kHz	
Open Lamp Shutdown Time	Ts-d	Lamp Open	1.0	1.5	2.0	sec	
Open Lamp Voltage	Vopen	Vin=24V, PDIM=100% Vopen=(V+) - (V-)	3240			Vrms	Note(2)
Enable	On	ON/OFF=High	2.4	-	5.5	V	
Off	ON/OFF=Low	-0.3	-	0.8	V		Off
External PWM Signal	Vpwm	ON(high)	2.4		5.5	V	
OFF(low)	-0.3		0.4	V		OFF(low)	-0.3
Duty	Active High	30	-	100	%	Note (3)	Duty
Fpwm	Vin=24V, Min	120	150	180	Hz		Fpwm

5. 포장 사양

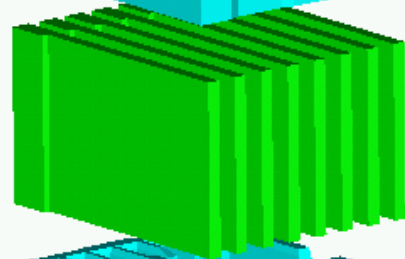
Packing -PalletBox



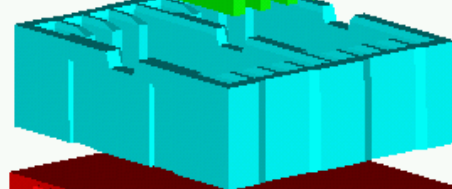
Cushion-Pallet



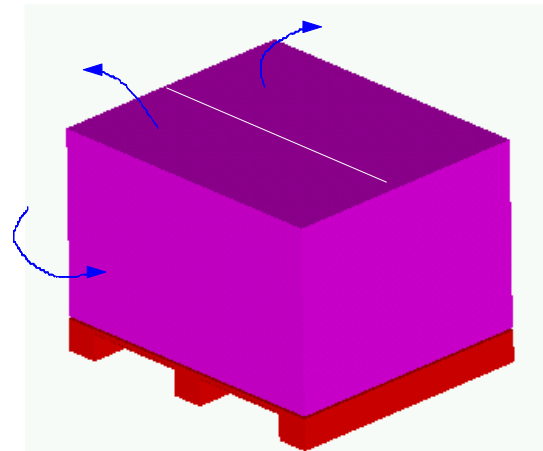
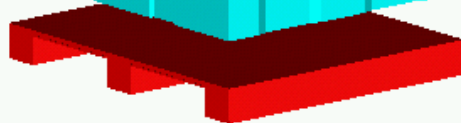
LCD Module



Cushion-Pallet



Pallet



→ Direction: be able to open it

- ▶ Packing Pallet Box
(LJ69-00866A, 단독)
- ▶ Cushion-Pallet
(LJ69-00867A, 단독)
- ▶ Module (8EA)
in Bag-Shielding
(6902-000665, 단독)
with Silica Gel 4ea/1Module
→ Module배면에 투입
- ▶ Cushion-Pallet
(LJ69-00867A, 단독)
- ▶ Pallet
(LJ69-01393A, 공용)